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APPLICANTS

Kazuko Inuzuka, Yokohama-shi, JAPAN;
 Kazuaki Kawaguchi, Kawasaki-shi, JAPAN;

** CONTINUING DATA *no LP*

** FOREIGN APPLICATIONS *yes LP*
 JAPAN 2002-287868 09/30/2002

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 2 7	INDEPENDENT CLAIMS 2 2
Verified and Acknowledged	Examiner's Signature _____ Initials <i>LP</i>				

ADDRESS
 000026021
 HOGAN & HARTSON L.L.P.
 500 S. GRAND AVENUE
 SUITE 1900
 LOS ANGELES, CA
 90071-2611

TITLE
 Synchronous semiconductor memory device of fast random cycle system and test method thereof

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